

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Dureseti CHIDAMBARRAO, *et al.*

Serial No.: 10/667,601

Filed: September 23, 2003

Docket No. FIS920030186US1

Confirmation No.: 9755

Group Art Unit: No. 2812

Examiner: Unassigned

For: **IMPROVED NFETS USING GATE INDUCED STRESS MODULATION**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CORRECTED FILING RECEIPT

Sir:

The undersigned respectfully requests a corrected Filing Receipt for the above-identified patent application. In particular, it is requested that the Title of the Invention "NFETs Using Gate Induced Stress Modulation" be changed to - - Improved NFETs Using Gate Induced Stress Modulation - - as reflected in the attached copy of the Declaration and Power of Attorney for Patent Application. Also enclosed is a copy of the date-stamped postcard, evidencing the filing of the Declaration and Power of Attorney with the U.S. Patent and Trademark Office on September 23, 2003. Also enclosed is a copy of the Filing Receipt with this correction marked in red ink.

Since the Inventor's name was correctly shown, issuance of a corrected Filing Receipt is in order. Since this error was due to the Patent and Trademark Office, no fee is submitted herewith.

Dureseti CHIDAMBARRAO, *et al.*
10/667,601

Please charge any deficiencies and credit any overpayments to attorney's deposit account
no. 23-1951.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Andrew M. Calderon". The signature is stylized with a large, loopy initial "A" and a long horizontal stroke extending to the right.

Andrew M. Calderon
Registration No.: 38,093

Date: January 21, 2004

McGuireWoods LLP
1750 Tysons Boulevard, Suite 1800
McLean, VA 22102
(703) 712-5000

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UNITED STATES PATENT AND TRADEMARK OFFICE

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/667,601	09/23/2003	2812	750	FIS920030186US1	10	20	3

CONFIRMATION NO. 9755

McGuireWoods LLP
 Suite 1800
 1750 Tyson Boulevard
 McLean, VA 22102

FILING RECEIPT



OC000000011711534

Date Mailed: 01/16/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Dureseti Chidambarrao, Weston, CT;
 Omer H. Dokumaci, Wappingers Falls, NY;
 Oleg G. Gluschenkov, Poughkeepsie, NY;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 01/16/2004

Projected Publication Date: 03/24/2005

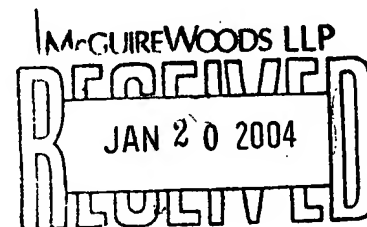
Non-Publication Request: No

Early Publication Request: No

Title

Improved NFETs using gate induced stress modulation

Preliminary Class



**LICENSE FOR FOREIGN FILING UNDER
Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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Inventors: Dureseti Chidambarao, et al.
Filing Date: Concurrently
For: IMPROVED NFETs USING GATE INDUCED STRESS
MODULATION

Date: September 23, 2003
Group Art.: Unassigned
Examiner: Unassigned
Atty. Docket: FIS920030186US1

REQUEST FOR EARLY NOTIFICATION OF SERIAL NUMBER

Commissioner for Patents:

Please place the Patent Office receipt stamp hereon to acknowledge receipt of the following:

1. A Utility Patent Application Transmittal (Large Entity);
2. 19 Pages of Specification, 20 Claims (3 Independent / 17 Dependent), Abstract;
3. 10 Sheets of Informal Drawings (Figs. 1, 2A, 2B, 2C, 2D, 2E, 2F, 2G 2H, 2I, 2J, 2K, 3A, 3B, 3C, 3D, 3E, 3F, 3G, 4 and 5);
4. An Executed Combined Declaration and Power of Attorney;
5. An Executed Assignment Form and an Assignment Recordation Cover Sheet;
6. An Information Disclosure Statement;
7. A PTO-1449 Form and accompanying art (7 documents); and
8. An acknowledgement postcard.

Andrew M. Calderon
Registration No. 38,093

00750476AA

HAND DELIVERED

22141 U.S. PTO

10/667601

Inventors: Dureseti Chidambarao, et al.
Filing Date: Concurrently
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